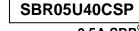


NOT RECOMMENDED FOR NEW DESIGN USE SDM05U40CSP





0.5A SBR[®] SUPER BARRIER RECTIFER

Product Summary

| V _{RRM} (V) | l _O (mA) | V _{Fmax} (V)@+25°C | I _{R max} (μΑ)@ +25°C |
|----------------------|---------------------|-----------------------------|--------------------------------|
| 40 | 500 | 0.46 | 75 |

Description and Applications

The SBR05U40CSP is a 40-volt 0.5A super barrier rectifier (SBR) that is optimized for low forward voltage drop and low leakage current, housed in a compact chip scale package (CSP) that occupies only 0.6mm² board-space. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space. It is ideally suited for use in portable applications as a:

- Blocking diode
- Boost diode
- Switching diode
- Reverse protection diode

Features and Benefits

- Off Board Profile of 0.3mm More Than 30% Thinner Than DFN1006
- Low forward voltage (VF) minimizes conduction losses and improving efficiency
- Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: X2-WLB1006-2
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Tin. Solderable per MIL-STD-202, Method 208 (e3)
- Polarity: Cathode Dot
- Weight: 0.001 grams



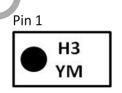
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---------------|--------------|-------------------|
| SBR05U40CSP-7 | X2-WLB1006-2 | 3,000/Tape & Reel |
| | | |

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.htmlfor more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



H3= Product Type Marking Code YM=Date Code Marking Y= Year (ex: A= 2013) M=Month (ex: 9= September) Dot denotes Cathode Pin

| Date Code Key | | | | | | | | | | | | | |
|---------------|-----|-----|------|-----|------|-----|------|-----|-----|------|-----|------|--|
| Year | 201 | 3 | 2014 | | 2015 | | 2016 | | | 2018 | 2 | 2019 | |
| Code | A | | В | | С | | D | | | F | | G | |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jul | Aug | Sep | Oct | Nov | Dec | |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D | |



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%. | | | |
|---|------------------|-------|------|
| Characteristic | Symbol | Value | Unit |
| Peak Repetitive Reverse Voltage | V _{RRM} | 40 | V |
| Average Rectified Output Current | Ιο | 500 | mA |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I _{FSM} | 12 | A |

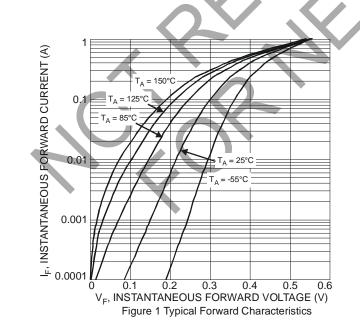
Thermal Characteristics

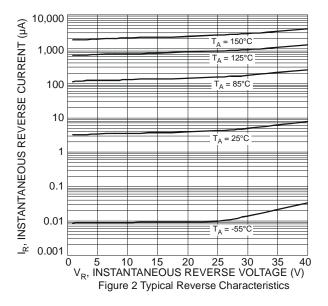
| Characteristic | Symbol | Value | Unit |
|---|------------------|-------------|------|
| Typical Thermal Resistance Junction to Ambient (Note 5) | R _{θJA} | 130 | °C/W |
| Operating and Storage Temperature Range | TJ, TSTG | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|--------------------------|----------------|-----|------|------|------|---|
| | | — | 0.30 | 0.36 | | I _F = 100mA |
| Forward Voltage Drop | VF | - | 0.43 | 0.46 | V | I _F = 500mA |
| | | | 0.40 | _ | | $I_F = 500 \text{mA}, T_J = +125^{\circ}\text{C}$ |
| | I _R | | 4 | 15 | | V _R = 10V |
| Reverse Current (Note 6) | | — | 8 | 75 | μA | $V_R = 40V$ |
| Junction Capacitance | Cj | | 34 | | pF | $V_R = 4V, f = 1MHz$ |

Notes: 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
6. Short duration pulse test used to minimize self-heating effect.

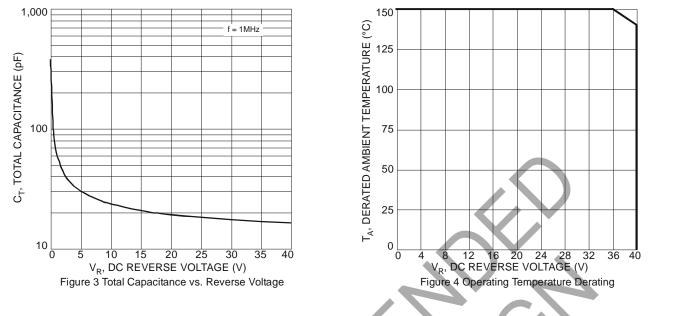




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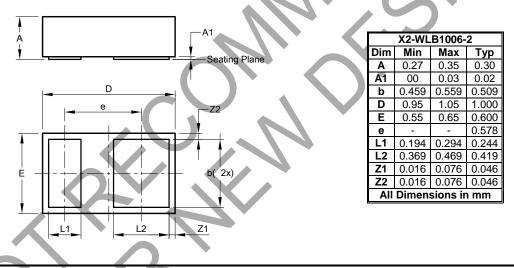


SBR05U40CSP



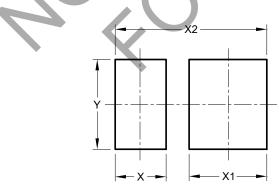
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) | | |
|------------|------------------|--|--|
| Dimensions | | | |
| Х | 0.332 | | |
| X1 | 0.507 | | |
| X2 | 0.989 | | |
| Y | 0.579 | | |

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